

Fig. 1

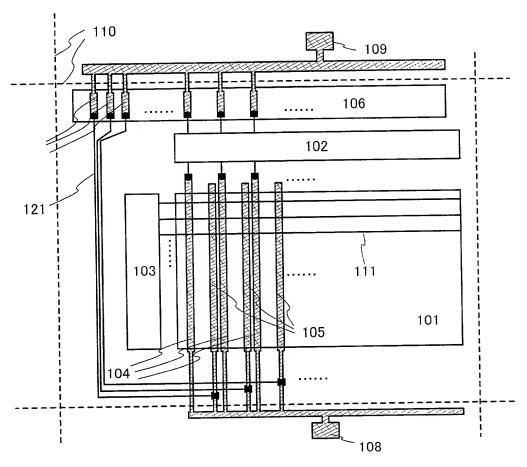


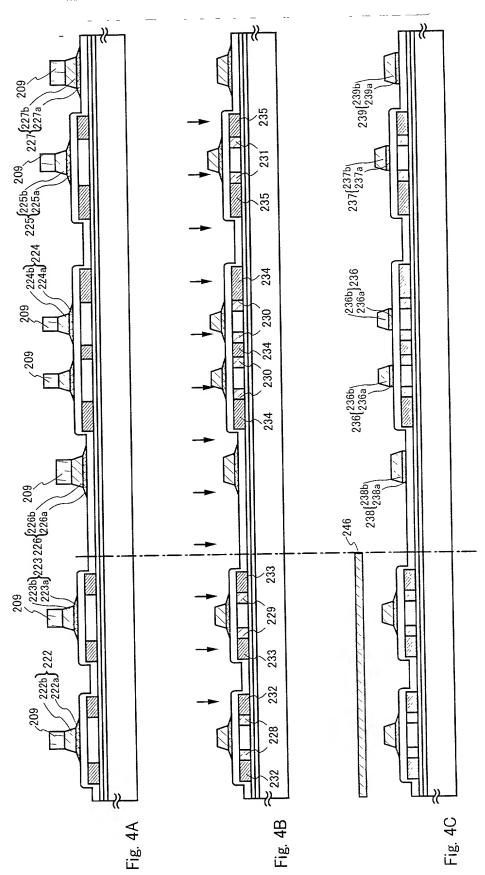
Fig. 2

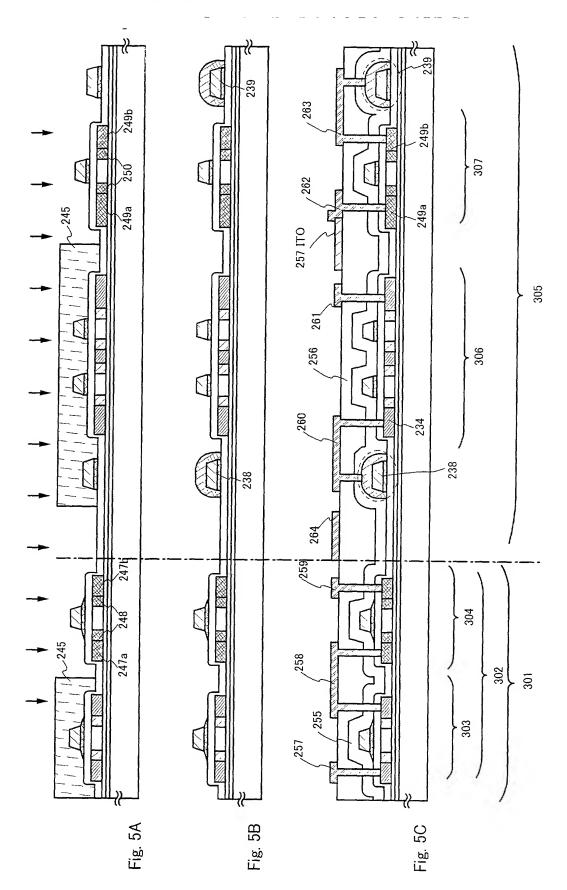
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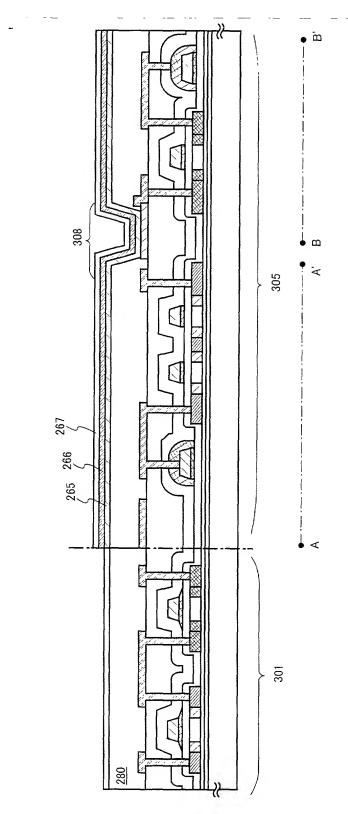
 $\binom{201b}{201a}$ 201 Fig. 3A

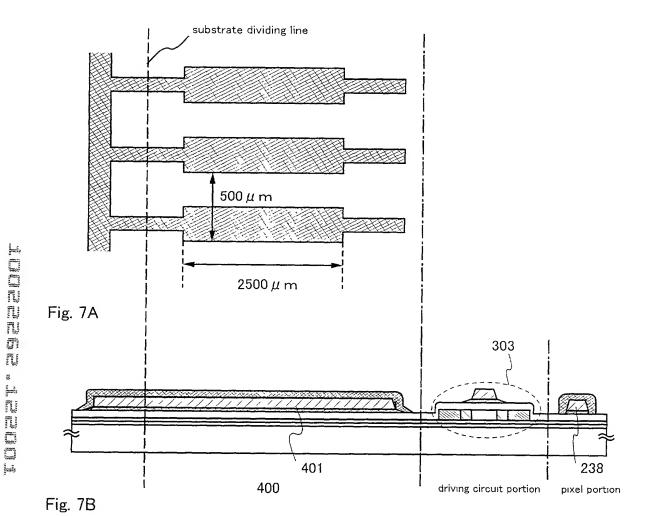




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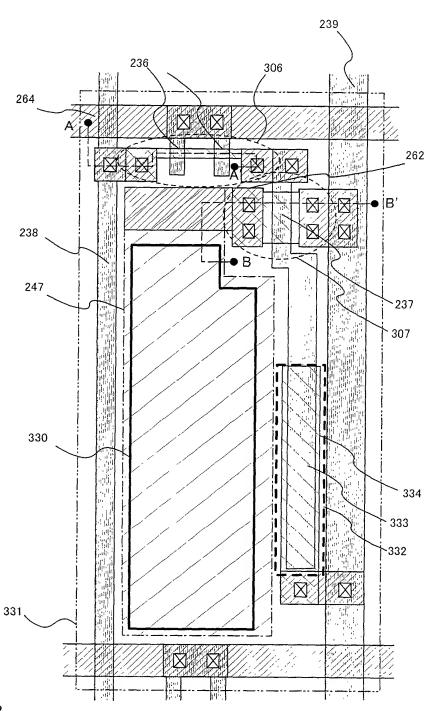
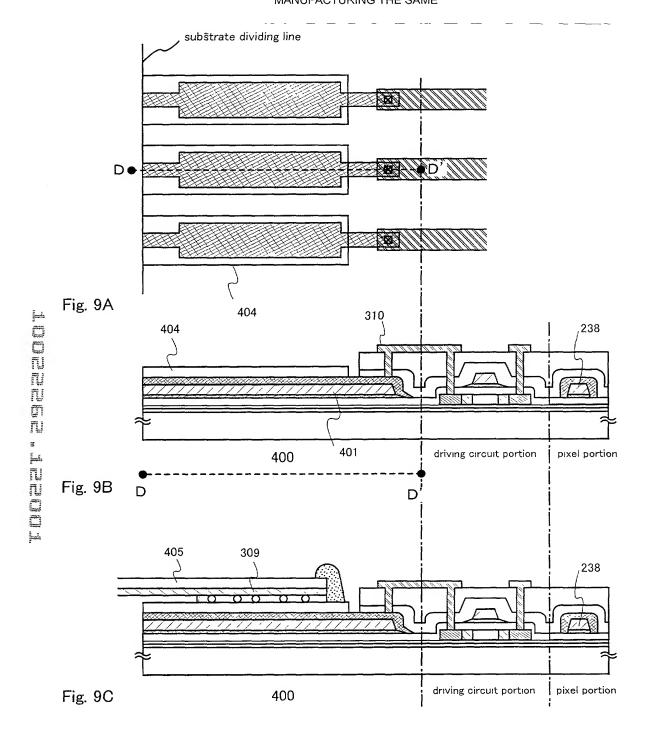
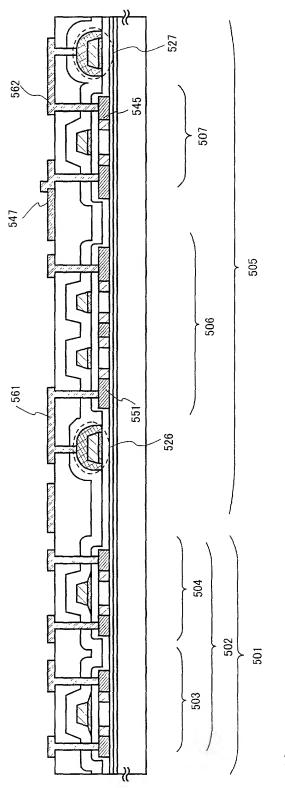


Fig. 8





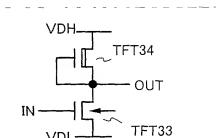
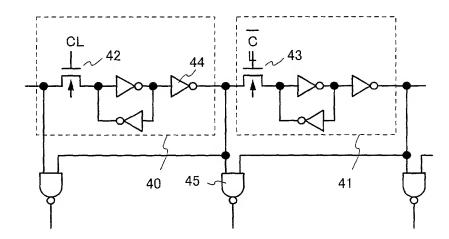


Fig. 11A EEMOS circuit

Fig. 11B EDMOS circuit



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Fig. 12A

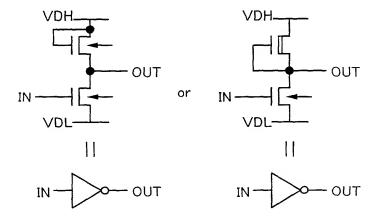


Fig. 12B

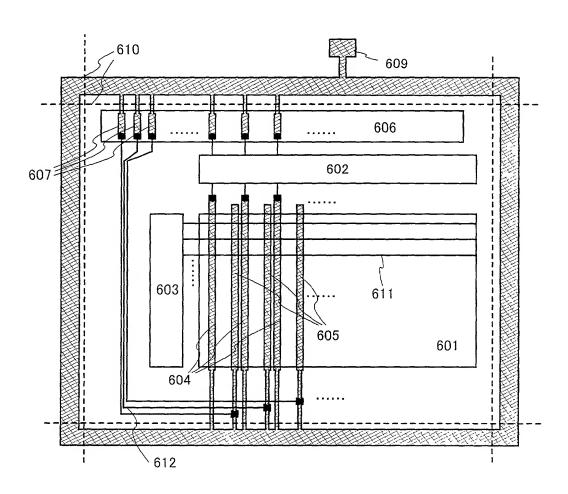
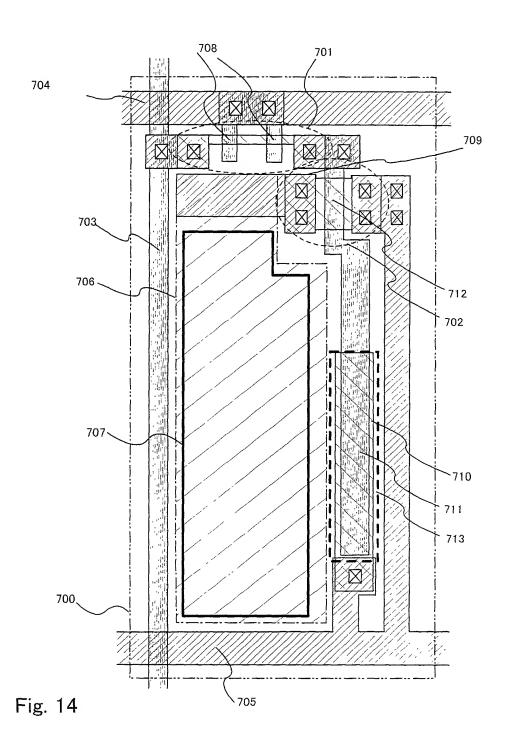


Fig. 13

MANUFACTURING THE SAME



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Fig. 15A 901 904 900. terminal portion driving circuit portion pixel portion

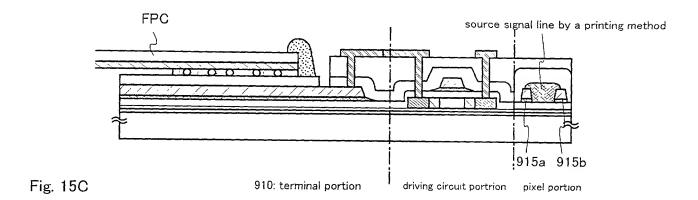
FPC

Fig. 15B 911 912 913

910: terminal portion

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driving circuit portrion

pixel portion

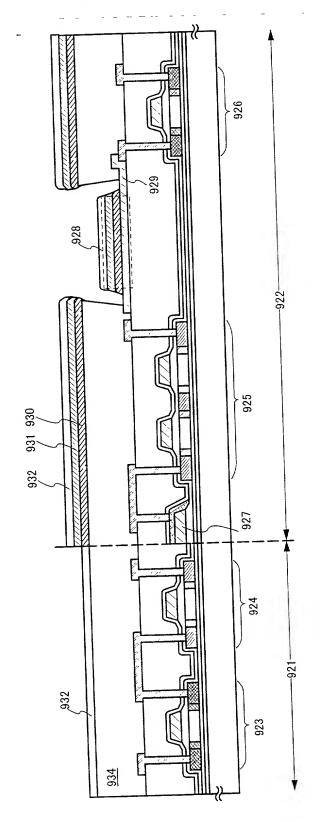
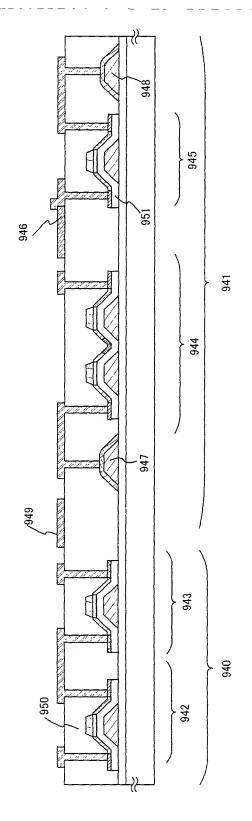
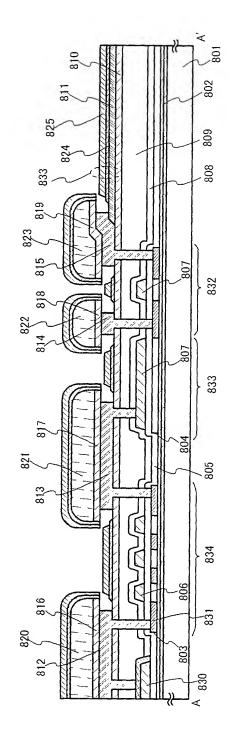


Fig. 16





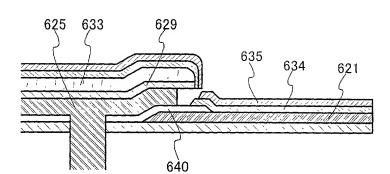


Fig. 19

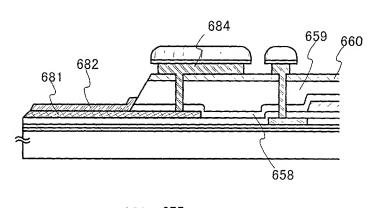


Fig. 20A

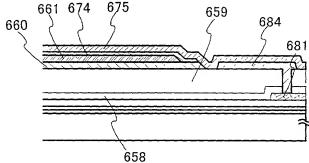


Fig. 20B

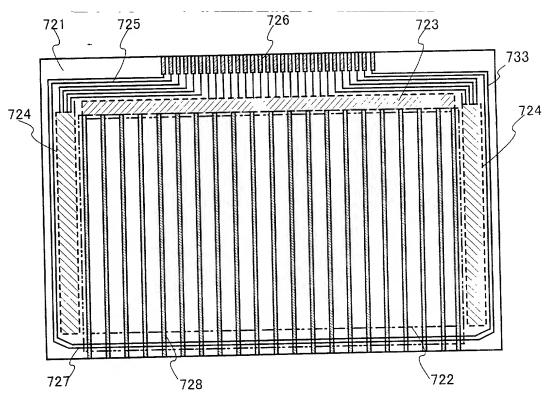


Fig. 21

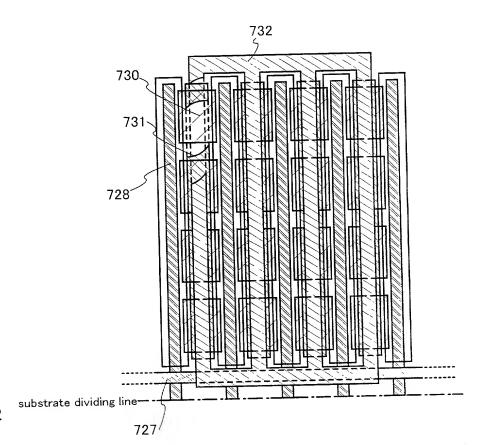


Fig. 22

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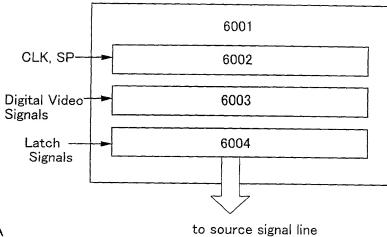


Fig. 23A

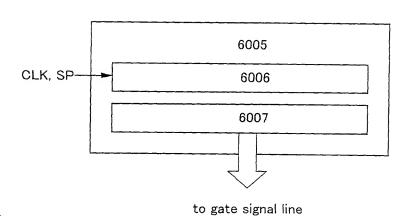


Fig. 23B

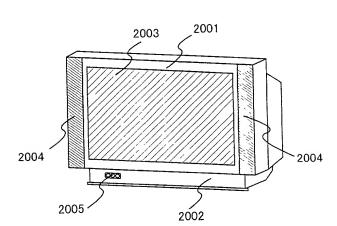


Fig. 24A

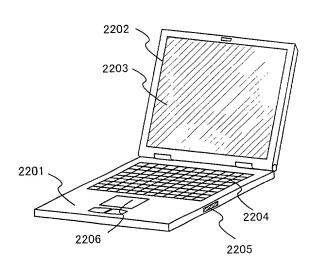


Fig. 24B

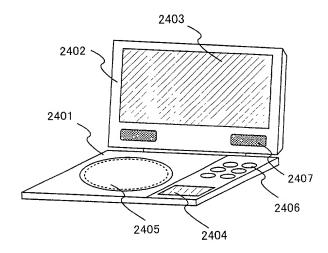


Fig. 24C

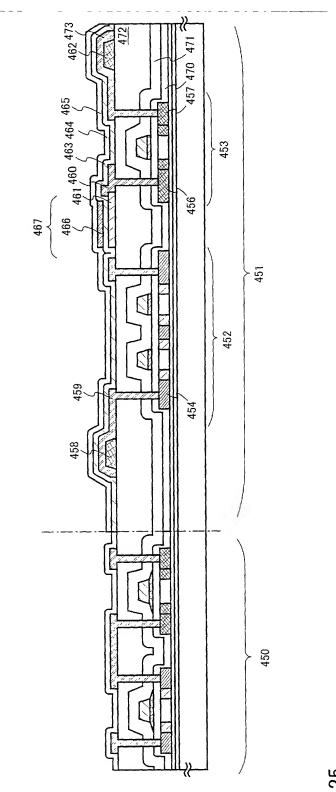


Fig. 25